Atos BullSequana S

Technical Support Bulletin

Product	SUBJECT		Dossier	Number	Rev
BullSequana Server	Memory exclusion on BullSequana S1600		400	20-01	1
Issue Date	**Writer**	**Approved by**	**Domain**	**Exp. Date**	
April 22, 2020	P. ARBENZ	D. COLIN	FIRMWARE	Undef.	

Issues:

After having excluded memory modules from a slave module on a BullSequana S1600 server, booting is not possible anymore.

Procedure / Workaround:

On a BullSequana S1600 server, when excluding memory modules is necessary, memory exclusion must be symmetrical between both memory controllers (iMC) of the processor (CPU). Otherwise booting will not be possible.

For example:

(1) If the memory modules in the L0 and L1 slots of CPU 1 need to be excluded, the memory modules in the H0 and H1 slots of CPU 1 must also be excluded.

(2) If the memory modules in the J0 and J1 slots of CPU 1 need to be excluded, the memory modules in the M0 and M1 slots of CPU 1 must also be excluded.

Example (1)   Example (2)

See: "Description Guide" (86A113FR) Chapter 4.4 for the full description of the memory module slots.

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